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Docket No. 0756-2149

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of HIL ROOM )  
Hisashi OHTANI et al. ) Art Unit: Unassigned  
Serial No. 09/593,765 ) Examiner: Unassigned  
Filed: 06/14/2000 )  
For: METHOD FOR MANUFACTURING )  
A SEMICONDUCTOR DEVICE )

## CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on SEP 5 2000.

INFORMATION DISCLOSURE STATEMENTREQUEST FOR CORRECTION

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, it is requested that the reference(s) listed on the attached Form PTO-1449 be made of record in the above-identified application.

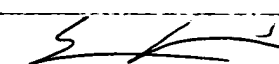
An Information Disclosure Statement was filed on June 14, 2000, that incorrectly listed the Japanese Patent Laid -Open No. 3-22540 as 3-25540. Further an Information Disclosure Statement was submitted on July 21, 2000, including a Full Translation of the 3-22540 patent, and a 1449 correctly listing 3-22540. Therefore, we request that the 3-25540 in the original 1449 filed July 14, 2000 be deleted or crossed out by the examiner form the 1449.

Furthermore, article (see Hazelden et al.) submitted on June 14, 2000, includes an incorrect date. Specifically, the date is listed as 1991 when it should be 1992. We request that the examiner correct this date on the 1449 when reviewing the Information Disclosure Statement.

The Commissioner is hereby authorized to charge fees under 37 C.F.R. §§1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 19-2380. A duplicate copy of this sheet is attached.

Respectfully submitted,

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Eric J. Robinson  
Reg. No. 38,285

NIXON PEABODY LLP  
8180 Greensboro Drive, Suite 800  
McLean, Virginia 22102  
(703) 790-9110